

FIG. 1

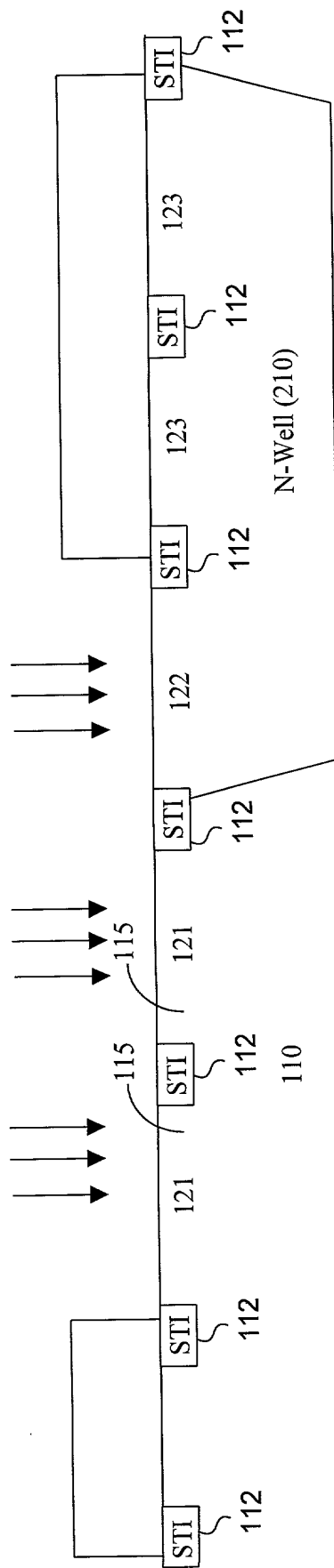
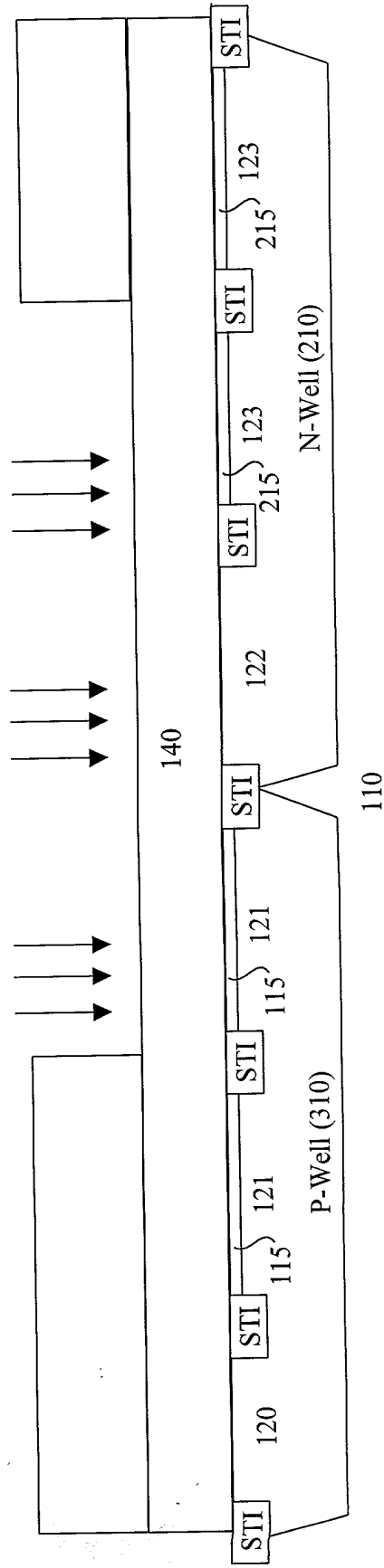
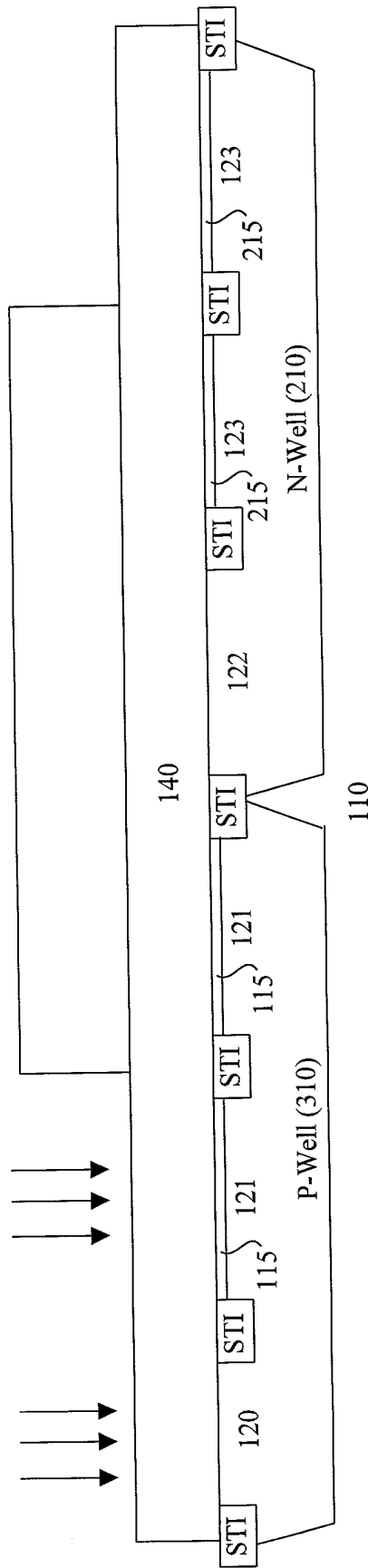


FIG. 2



N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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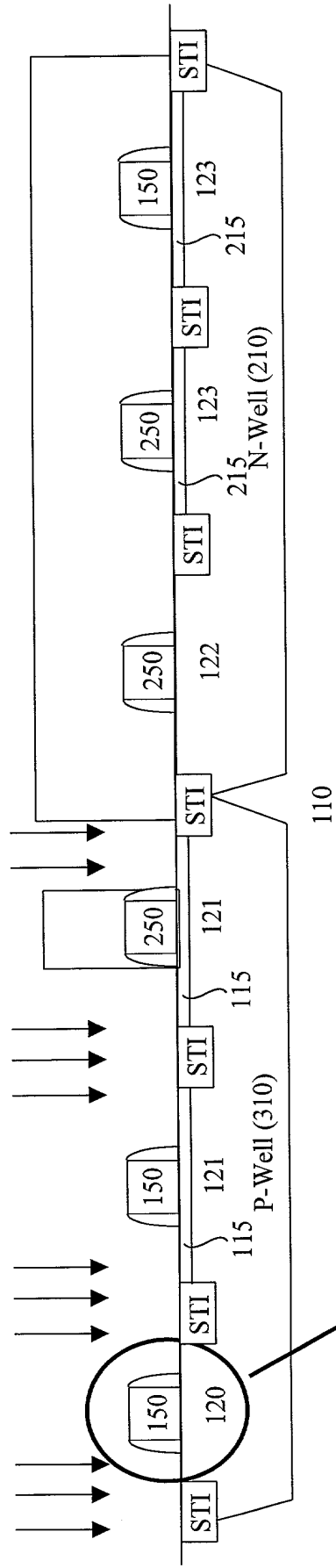


FIG. 9A

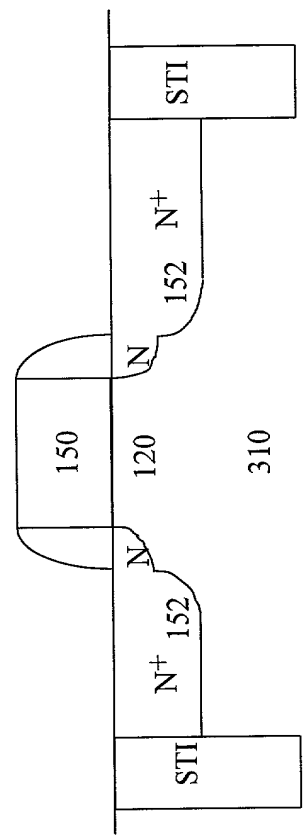


FIG. 9B

N-Poly NMOS Surface Channel	N-Poly NMOS Buried Channel	P-Poly NMOS Buried Channel	P-Poly PMOS Surface Channel	P-Poly PMOS Buried Channel	N-Poly PMOS Buried Channel
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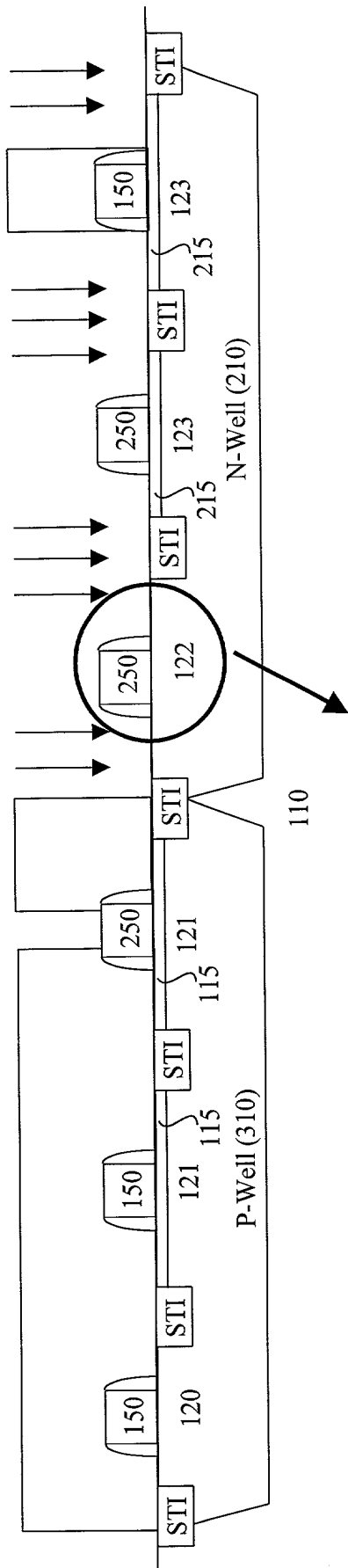


FIG. 10A

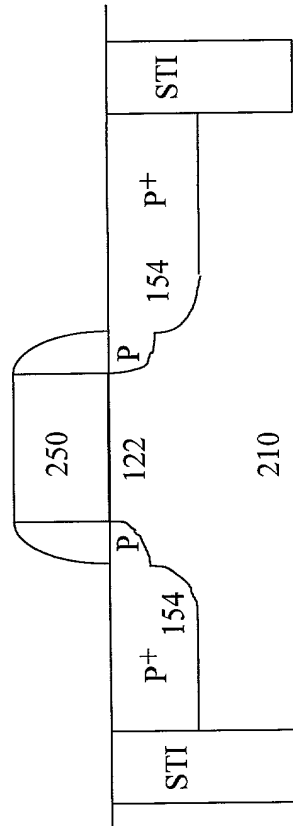
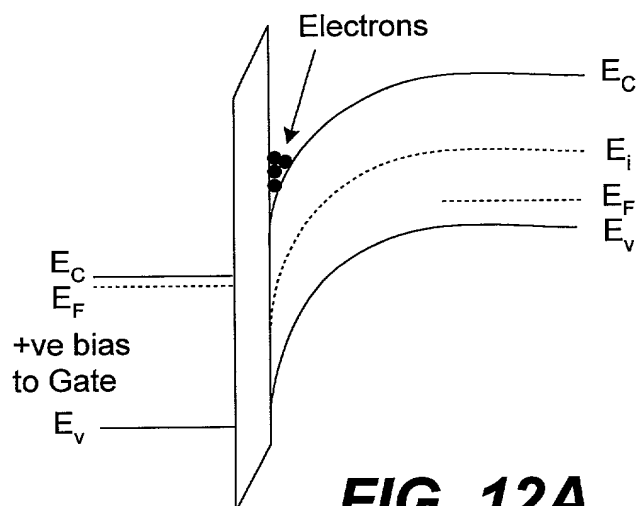
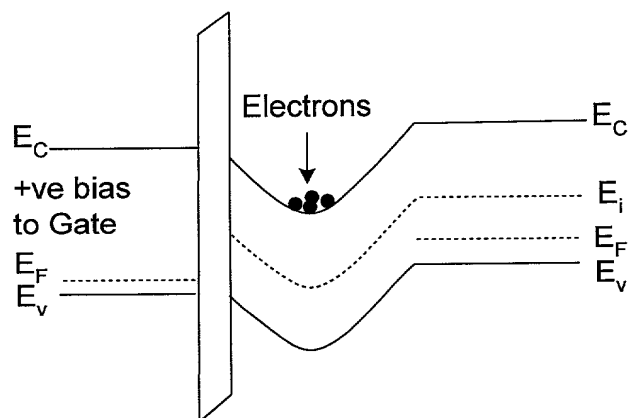


FIG. 10B



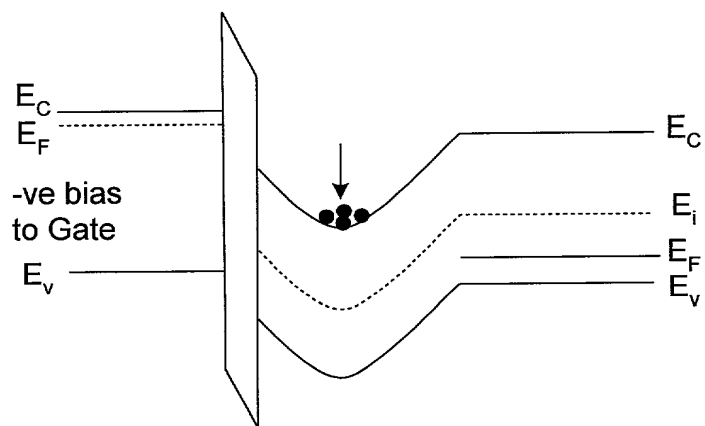


**FIG. 12A**  
(Prior Art)



**FIG. 12B**





**FIG. 12C**